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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Cherukuri et al.

Docket Number: TI-36594

Serial No.: 10/806,520

Art Unit: 2818

Filed: 03/23/2004

Examiner: T. Nguyen

VERTICALLY STACKED SEMICONDUCTOR DEVICE

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NAME OF INVENTOR(S): CHERUKURI ET AL. TITLE OF INVENTION: VERTICALLY STACKED SEMICONDUCTOR		Serial No.: 10/806,520 Conf. No.: 8382					
TI-36594	20-0668						
FAXED: 03/09/2006 DUE: 03/13/2006 ATTY/SECY: YT/jsm							

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Texas Instruments Incorporated PO Box 655474, M/S 3999 Dallas, TX 75265



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE PATENT APPLICATION

Appl. No.

10/806,520

Confirmation No. 2743

Applicant

Kalvan C. Cherukuri et al.

Filed

March 23, 2004

TC/A.U.

2818

Examiner

Nguyen, Thinh T

Docket No.

TI-36594

Customer No.

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Commissioner for Patents P. O. Box 1450

Alexandria, VA 22313-1450

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Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed Form PTO 1449. This citation was recently made known to applicants via a search report by the International Searching Authority in a counterpart application and was mailed on Dec 13, 2005.

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Respectfully submitted,

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FORM PTO-1449	149 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO.			SERIAL NO.		
(REV. 7-80) PATENT AND TRADEMARK OFFICE			TI-36594						
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